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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/688,083
Applicant : Umemoto, et al
Filed : October 16, 2003
TC/A.U. : 2826
Examiner : Scott R. Wilson
Title : SEMICONDUCTOR INTEGRATED CIRCUIT MAKING USE
OF STANDARD CELLS

Docket No. : 2102487-991121
Customer No. : 26379

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Rosa A. Caviedes

RESPONSE TO OFFICE ACTION OF JUNE 28, 2004

Sir:

In response to the Office Action of June 28, 2004, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Drawings begin on page 5 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 6 of this paper.

An Appendix including amended drawing figures is attached following page 8 of this paper.